

SINE WAVE GENERATION CIRCUITABSTRACT OF THE DISCLOSURE

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A sine wave generation circuit for generating a high precision pseudo sine wave without increasing the circuit size including a pulse generation circuit for generating a plurality of pulse signals using an input clock signal as reference and a voltage output circuit for stepwisely changing the voltage level of the output on the basis of a plurality of pulse signals. The voltage output circuit generates a plurality of different coefficients in response to a combination of bit information in a plurality of pulse signals and changes the output of the voltage output circuit in response to a coefficient series obtained when further combining the plurality of generated coefficients, and odd number generation circuits each generating odd number values of ternary-values or more having positive side coefficients and negative side coefficients symmetric about a center coefficient are provided in a plurality of coefficient generation circuits provided in the voltage output circuit.